

## **AMENDMENTS TO THE SPECIFICATION**

Please replace the section of the original specification entitled "ABSTRACT OF THE DISCLOSURE" with the following replacement section. In accordance with 37 C.F.R. §1.121(b)(2), a version of such replacement section marked up to show changes relative to the original section is presented below. For the Examiner's convenience, a clean copy of this replacement section is provided with this response as Appendix A. The present amendments to the Abstract of the Disclosure are in response to the Examiner's reminders for keeping the Abstract to within a recommended 150 word limit and to exclude form and legal phraseology often used in patent claims from the Abstract. Such amendments do not add any new subject matter to the present application.

## **ABSTRACT OF THE DISCLOSURE**

A window via capacitor ~~comprises~~includes a stacked ~~multilayer~~ configuration of at least one bottom layer, a plurality of first and second layers, a transition layer and a cover layer. ~~An alternative window via capacitor comprises a stacked configuration of a~~Alternatively, bottom window layer, a bottom and transition layers, a plurality of first and second layers, followed by ~~a top window layer and a top~~ cover layers are respectively provided. ~~Each~~First and second layers are ~~is preferably~~ characterized by ~~a~~respective sheets of dielectric material with ~~an~~ respective first or second electrode plate provided thereon, adjacent pairs of. ~~Adjacent first and second~~ electrode plates forming opposing active capacitor plates ~~in the multilayer configuration~~. Portions of each ~~first and second~~ electrode plate as well as electrode portions provided on each transition layer ~~extend to and~~ are exposed on selected side portions of the ~~periphery of the~~ window via capacitor periphery, such that terminations can connect respective first and second polarity electrodes together. ~~Electrode portions of each transition layer are aligned in respective similar locations to the first and second electrode plates such that peripheral terminations can connect selected electrode portions of a first polarity together~~

~~and selected portions of the opposing polarity together. In some embodiments, the connection of peripheral terminations to the electrode portions of the transition layer collect the two opposing terminations onto a single planar surface.~~ Window vias may then be formed through windows provided in the cover layers to effect low inductance electrical connection to the active components of the window via capacitor. ~~Solder balls may also be applied to such window vias to yield a capacitor compatible with BGA mounting technology.~~